REMARKS

Applicant has amended claims 3 and 4 to correct minor matters of English usage without changing claim scope and added new claim 5.

Applicant thanks the Examiner for indicating allowable subject matter.

Claim 1 has been rejected under 35 USC 102(e) as anticipated by U.S. Patent No. 6,879,145 (Yamazaki). Applicant respectfully traverses this rejection.

Claim 1 recites a driving transistor provided for each of the pixels to drive a corresponding electroluminescent element according to a display signal supplied through a corresponding pixel selecting transistor and states that the driving transistor comprises a channel of a P type and a lightly-doped-drain structure. The claimed driving transistor reduces leakage between the gate and the source of the driving transistor. See, for example, page 5, lines 19-20, of the specification.

The Examiner contends that Yamazaki's current controlling FET 204 shown in FIG. 2A corresponds to the claimed driving transistor because the p-channel FET 201 shown in Yamazaki's FIG. 1 discloses, in the Examiner's view, the claimed channel of a P type of the driving transistor. Applicant respectfully disagrees.

Yamazaki's current controlling FET 204 is provided for each of Yamazaki's pixels to drive a corresponding electroluminescent element 205 according to a display signal supplied through a corresponding pixel selecting transistor 203, as is the case with the claimed driving transistor. However, Yamazaki's current controlling FET 204 is an n-channel FET and is not a p-channel FET as claimed. See, for example, column 3, lines 34-39, of Yamazaki. Yamazaki's FET 201, which is relied upon by the Examiner for the teaching that the claimed driving transistor comprises a channel of a P type, is part of Yamazaki's driver circuit, such as a shift register circuit, a buffer circuit, a sampling circuit, a D/A converter or a latch circuit. See, for example, column 5, line 65- column 6, line 6, of Yamazaki. Accordingly, Yamazaki's p-channel FET 210 is not a driving transistor that drives an electroluminescent element as claimed. No part

of Yamazaki teaches or suggests that Yamazaki's current controlling FET 204 comprises a channel of a P type as claimed.

The rejection of claim 1 under 35 USC 102(e) on Yamazaki should be withdrawn because Yamazaki does not teach or suggest the claimed driving transistor that comprises a channel of a P type.

New claim 5 finds support at page 5, lines 2-6, of the specification.

In light of the above, a Notice of Allowance is solicited.

In the event that the transmittal letter is separated from this document and the Patent and Trademark Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952**, referencing Docket No. 492322016700.

Respectfully submitted,

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By: _____

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